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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Stephen Henry Kolokowsky et al.
Serial No.: 10/796,872 Examiner: Jeffrey Gaffin
Filing Date: March 8, 2004 Group Art Unit: 2182
Title: UNIVERSAL SERIAL BUS INTERFACE TO MASS STORAGE DEVICE
Confirmation No. 2672

Mail Stop Amendment
Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

This Information Disclosure Statement is submitted:

under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under §1.114; whichever occurs last)

under 37 CFR 1.97 (c) together with either a:
 Statement under 37 CFR 1.97 (e), or
 a \$180 fee under 37 CFR 1.17 (p); or

under 37 CFR 1.97 (d) together with:
 Statement under 37 CFR 1.97 (e), and
 a \$180.00 fee set forth in 37 CFR 1.17 (p).

Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

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Respectfully submitted,
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
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Lauren Ballard-Gemmell



PATENT APPLICATION
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**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.</u>
—	—	<i>Universal Serial Bus Mass Storage Class Specification Overview Revision 1.2</i> USB Implementers Forum June 23, 2003 Pages 1-7
—	—	<i>Universal Serial Bus Mass Storage Class Bulk-Only Transport Revision 1.0</i> USB Implementers Forum September 31, 1999 Pages 1-22
—	—	<i>Universal Serial Bus Mass Storage Class Control/Bulk/Interrupt Revision 1.0</i> USB Implementers Forum December 14, 1998 Pages 1-26
—	—	<i>CY4611 – FX2 USB to ATA/CF Reference Design Notes</i> Cypress Semiconductor Corporation November 2000 – July 2002 Pages 1-7
—	—	<i>CY7C68013 EZ-USB® FX2™ USB Microcontroller High-Speed USB Peripheral Controller</i> Cypress Semiconductor Corporation June 21, 2002 Pages 1-50
—	—	<i>Advanced Technology Attachment ATA-2</i> From: PC Guide.com January 2003 Pages 1-14

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EZ-USB FX2 Technical Reference Manual Version 2.1
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Pages 1-130

Examiner: _____

Date Considered: _____